

Electronic Acknowledgement Receipt

EFS ID:	1314878
Application Number:	09866822
International Application Number:	
Confirmation Number:	5104
Title of Invention:	Circuitry for Mitigating performance loss associated with feedback loop delay in decision feedback equalizer and method therefor
First Named Inventor/Applicant Name:	Dagnachew Birru
Customer Number:	24737
Filer:	Michael E. Marion/NOEMI CHAPA
Filer Authorized By:	Michael E. Marion
Attorney Docket Number:	US 010264
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Application Type:	Utility

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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)	Multi Part /.zip	Pages (if appl.)
1		US010264_amd_14-NOV-2006.pdf	140546	yes	9

Multipart Description/PDF files in .zip description			
Document Description		Start	End
Amendment - After Non-Final Rejection		1	1
Claims		2	3
Applicant Arguments/Remarks Made in an Amendment		4	9

Warnings:

Information:

Total Files Size (in bytes):	140546
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 If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371
 If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.